

Electronic Acknowledgement Receipt

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International Application Number:	
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Title of Invention:	System and method for optimizing interconnections of components in a multichip memory module
First Named Inventor/Applicant Name:	Jeffery W. Janzen
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part / .zip	Pages (if appl.)
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1	Information Disclosure Statement (IDS) Filed	501286-01idsasfiled071508.PDF	525914 6f6c7d378d5ae0294cde85c06849d3ce766d05	no	10
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8230 abd7bd8d8753a2b4a333916e558ee0090e48b3	no	2
Warnings:					
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Total Files Size (in bytes):			534144		
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